



PRIOR ART

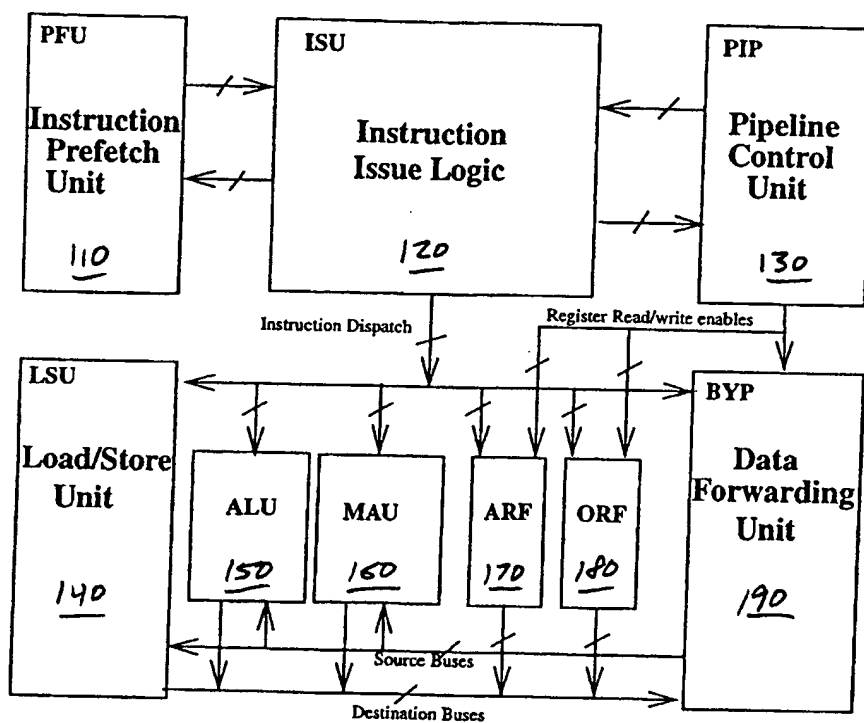


FIG. 1

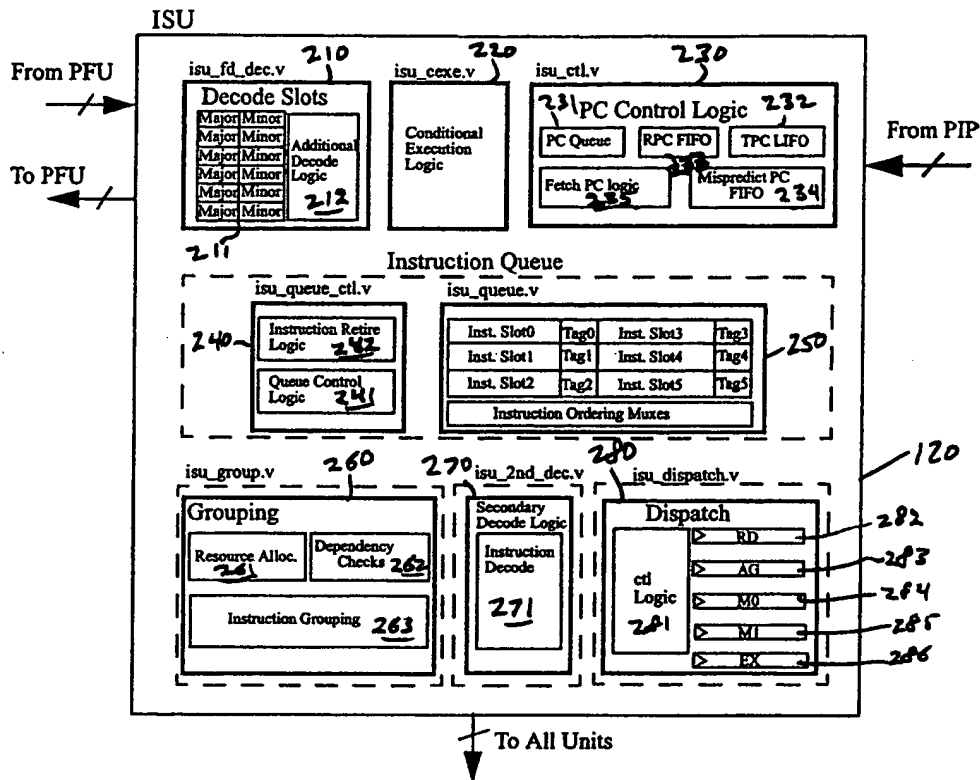


FIG. 2

310

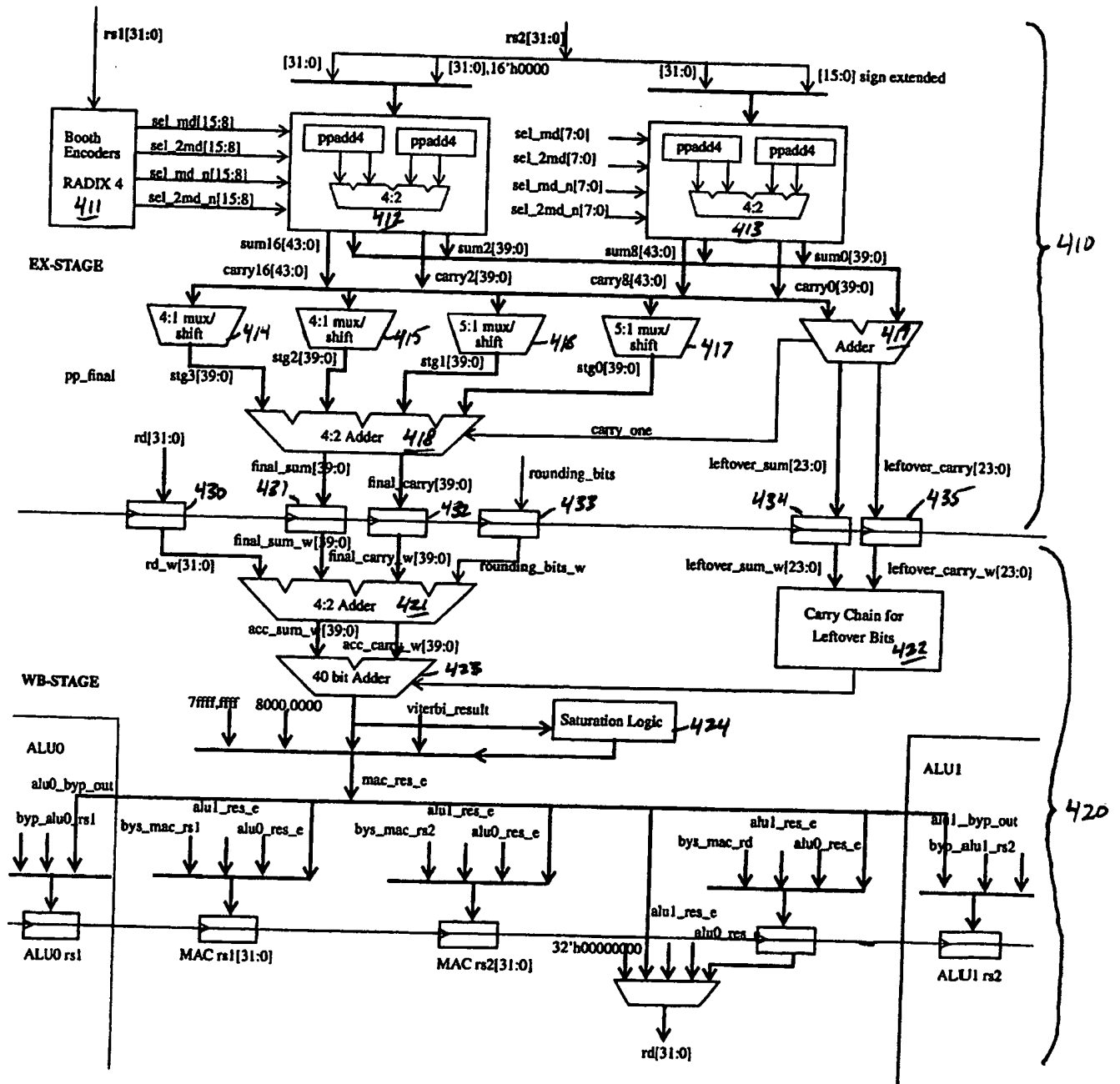


FIG. 4

```

loop1:
  ldu  r2, r12, 1  ! r2 = mem[r12], r12=r12+1
  ldu  r4, r13, 1  ! r4 = mem[r13], r13=r13+1
  mac.a r2, r4      ! {g0 r1 r0} += r2 * r4
  agn0 loop1        ! repeat loop1
  
```

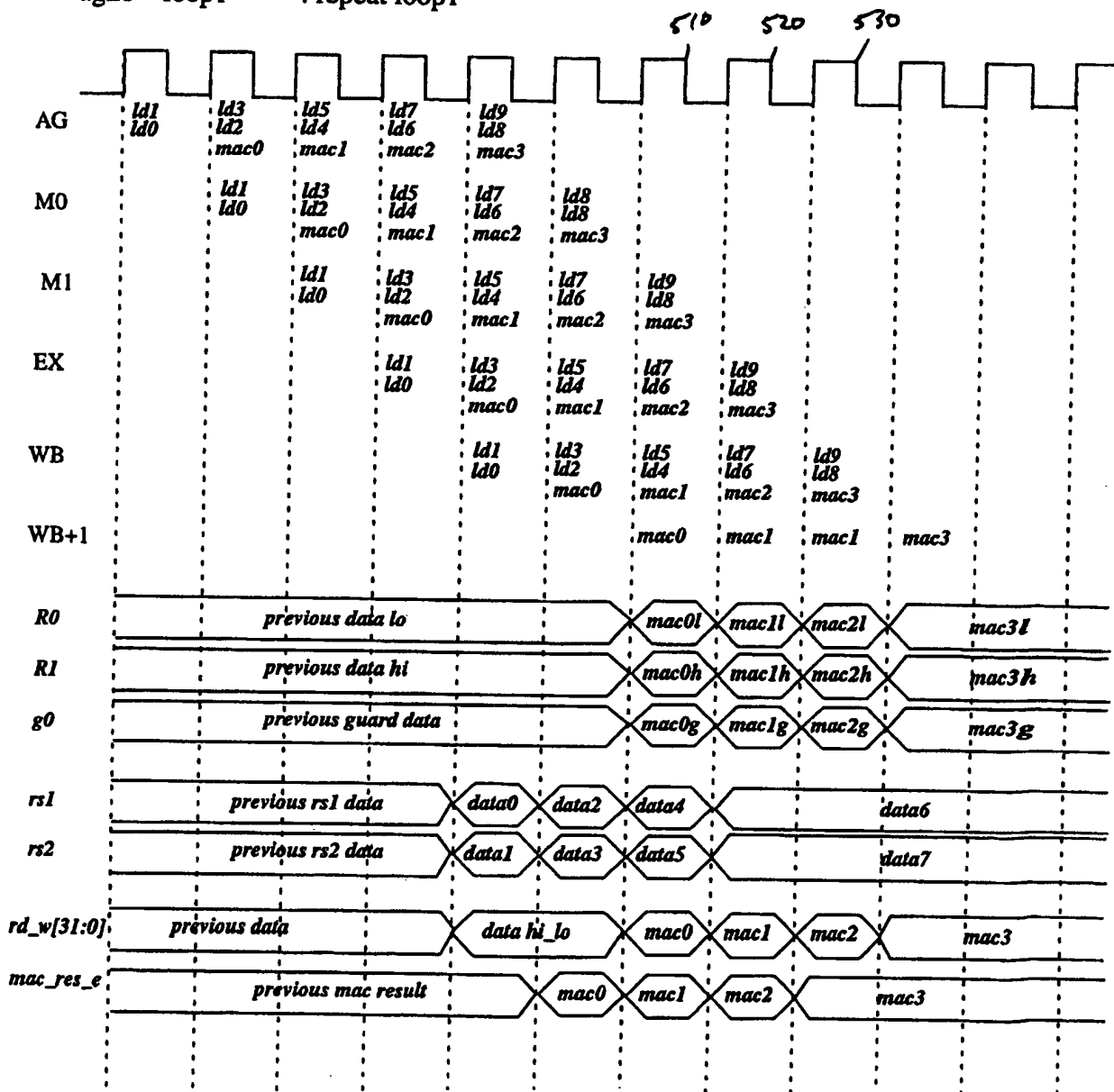


FIG. 5

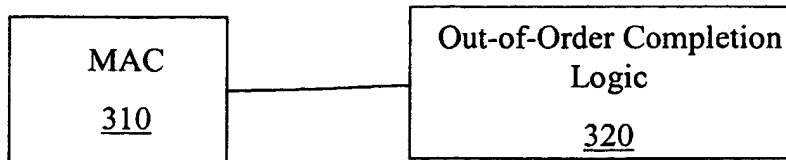


FIG. 3

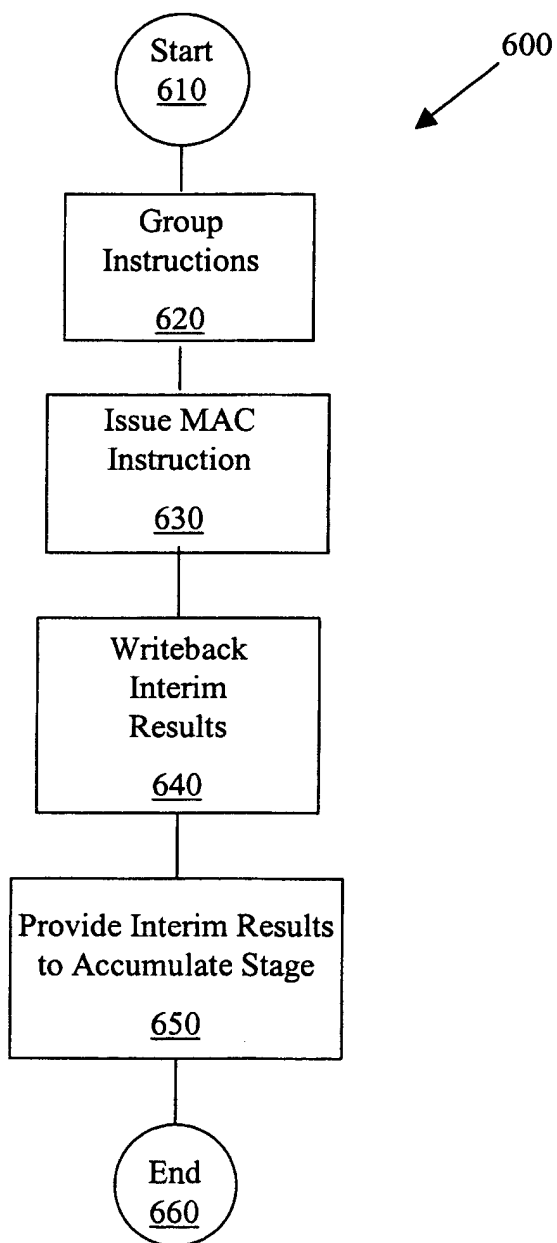


FIG. 6